

05-04-07 IFW  
Yu, Zunhang, et al.  
U.S. Application No.: 10/821,466



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:	)	
	)	Group Art Unit: 2825
Yu, Zunhang et al.	)	
	)	Examiner: Mr. Paul Dinh
Application No.: 10/821,466	)	
	)	Atty Docket No.: A1168
Filed: April 8, 2004	)	
	)	REQUEST FOR
For: APPARATUS AND METHOD FOR RTL	)	CORRECTED FILING
BASED FULL CHIP MODELING OF A	)	RECEIPT
<u>PROGRAMMABLE LOGIC DEVICE</u>	)	

May 1, 2007

Office of Initial Patent Examination  
Filing Receipt Corrections  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir,

Attached is a Declaration executed by inventor Zunhang Yu and a marked-up copy of the Corrected Filing Receipt for the above-referenced case for which issuance of a corrected Filing Receipt is respectfully requested.

There is an error in Applicant(s) section of the Filing Receipt. The first inventor's name is misspelled. The first inventor's name was also misspelled on the original Declaration. A new Declaration reflecting the correct spelling is attached. The correct Applicant(s) section should read as follows (with corrections noted in underscored text):

Zunhang Yu, Sunnyvale, CA

Ninh D. Ngo, Palo Alto, CA

Guy Dupenloup, Redwood City, CA

Yu, Zunhang, et al.  
U.S. Application No.: 10/821,466

Per current PTO guidelines, there is no charge for correcting the Filing Receipt. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 502300. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account. Applicants respectfully request the Filing Receipt to be updated

Respectfully Submitted,

A handwritten signature in black ink, appearing to read 'Elaine K. Lee', with a stylized, flowing script.

Elaine K. Lee  
Reg No. 41,936

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## UNITED STATES PATENT AND TRADEMARK OFFICE

MAY 02 2007

UNITED STATES DEPARTMENT OF COMMERCE  
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APPL NO.	FILING OR 371 (DATE)	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/821,466	04/08/2004	2625	770	A1168	10	16	2

25004  
 ALTERA CORPORATION  
 101 INNOVATION DR  
 SAN JOSE, CA 95134

A1168

AUG 20 2004

CONFIRMATION NO. 1091

CORRECTED FILING RECEIPT



\*OC000000013552376\*

Date Mailed: 08/16/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s) Zunhang  
~~Zunhang~~ Yu, Sunnyvale, CA;  
 Ninh Ngo, San Jose, CA;  
 Guy Dupenloup, Redwood City, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 06/25/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Apparatus and method for RTL based full chip modeling of a programmable logic device

Preliminary Class

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**LICENSE FOR FOREIGN FILING UNDER  
Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

**GRANTED**

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**NOT GRANTED**

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**DECLARATION FOR PATENT APPLICATION**

I, a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below, next to my name.

I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

APPARATUS AND METHOD FOR RTL BASED FULL CHIP MODELING OF A  
PROGRAMMABLE LOGIC DEVICE

the specification of which

\_\_\_\_\_ is attached hereto.

X was filed on April 8, 2004 as  
United States Application Number 10/821,466  
or PCT International Application Number \_\_\_\_\_  
and was amended on \_\_\_\_\_

\_\_\_\_\_ was filed as PCT international Application No. \_\_\_\_\_ on \_\_\_\_\_ and was  
amended under Article 19 on \_\_\_\_\_ (if  
applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claim(s), as amended by any amendment referred to above.

I do not know and do not believe that the claimed invention was ever known or used in the United States of America before my invention thereof, or patented or described in any printed publication in any country before my invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, and that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months (for a utility patent application) or six months (for a design patent application) prior to this application.

I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or

365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed:

APPLICATION NUMBER	COUNTRY	DATE FILED (DAY, MONTH, YEAR)	PRIORITY CLAIMED

I hereby claim the benefit under title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below

PROVISIONAL APPLICATION NUMBER	FILING DATE

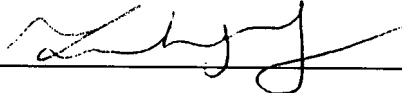
I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or 365(c) of any PCT International application designating the United states of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

NON-PROVISIONAL APPLICATION SERIAL NO.	FILING DATE	STATUS		
		PATENTED	PENDING	ABANDONED

Address all correspondence to and direct all telephone calls to **Elaine K. Lee** (Reg. No. **41,936**), Legal Department, Altera Corporation, 101 Innovation Drive San Jose, CA 95134, Telephone (408) 544-7591.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

**Full Name of First/Joint Inventor** Zunhang Yu

Inventor's Signature  Date 5/1/2007

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**Full Name of Second/Joint Inventor** Ninh D. Ngo

Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_

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**Full Name of Third/Joint Inventor** Guy Dupenloup

Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_

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